



are Application of
Katoh et al.

Serial No.: 09/711,504

Group Art Unit: 2826

Filed: November 14, 2000

Examiner: Flynn, N.

For: THIN FILM TRANSISTOR AND FABRICATION METHOD OF THE SAME

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

**RESPONSE TO FIRST OFFICE ACTION AND RESTRICTION
REQUIREMENT**

Sir:

In response to the Office Action dated December 12, 2001, please consider the following:

REMARKS

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group I (e.g., the semiconductor device as defined by claims 1-7), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Further, Applicant respectfully requests the Examiner's acknowledgment of the priority document filed on November 14, 2000.

Early, favorable prosecution on the merits is respectfully requested.

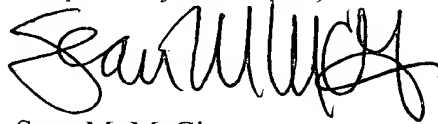
Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.

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2

A conditional petition is made for any extension of time which may become necessary.
The Commissioner is authorized to charge any fees for such extension and to credit any
overpayment in fees to Attorney's Deposit Account No. 50-0481.

Respectfully Submitted,

A handwritten signature in black ink, appearing to read "Sean McGinn", with a stylized flourish at the end.

Sean M. McGinn

Reg. No. 34,386

Date: January 11, 2002
McGinn & Gibb, PLLC
8321 Old Courthouse Road, Suite 200
Vienna, Virginia 22182-3817
(703) 761-4100
Customer No. 21254